

Day : Thursday
Date: 10/19/2006
Time: 23:16:18

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = GHIASI

First Name = ALI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09650715	6546345	150	08/30/2000	SYSTEM AND METHOD FOR MEASURING EXTINCTION RATIO AND DETERMINISTIC JITTER	GHIASI, ALI
10361255	Not Issued	30	02/10/2003	High-speed serial bit stream multiplexing and demultiplexing integrated circuits	GHIASI, ALI
10361463	Not Issued	30	02/10/2003	Source centered clock supporting quad 10 GBPS serial interface	GHIASI, ALI
10390490	Not Issued	30	03/17/2003	Loop back testing structure for high-speed serial bit stream TX and RX chip set	GHIASI, ALI
10390495	Not Issued	71	03/17/2003	Conditioning circuit that spectrally shapes a serviced bit stream	GHIASI, ALI
10393613	Not Issued	71	03/21/2003	Bit stream conditioning circuit having output pre-emphasis	GHIASI, ALI
10393639	Not Issued	30	03/21/2003	Natural data ordering of a multiplexed high speed bit stream	GHIASI, ALI
10417990	Not Issued	41	04/17/2003	Bit stream linear equalizer with AGC loop	GHIASI, ALI
10418009	Not Issued	41	04/17/2003	Bit stream conditioning circuit having adjustable input sensitivity	GHIASI, ALI
10418035	Not Issued	41	04/17/2003	Bit stream conditioning circuit having adjustable PLL bandwidth	GHIASI, ALI
10602226	Not Issued	30	06/24/2003	Multi-stage high speed bit stream demultiplexer chip set having switchable master/slave relationship	GHIASI, ALI
10602227	Not Issued	30	06/24/2003	Multi-stage multiplexing chip set having switchable forward/reverse clock relationship	GHIASI, ALI
10625438	Not Issued	30	07/23/2003	Multiple high-speed bit stream interface circuit	GHIASI, ALI
10767729	Not Issued	30	01/30/2004	Method of monitoring the quality of a communications channel	GHIASI, ALI
10767748	Not Issued	20	01/30/2004	System for monitoring the quality of a communications channel with mirror receivers	GHIASI, ALI
10779001	Not Issued	30	02/13/2004	Method and system for onboard bit error rate (BER) estimation in a port bypass	GHIASI, ALI

				controller	
10779232	Not Issued	61	02/13/2004	Method and system for robust elastic FIFO (EFIFO) in a port bypass controller	GHIASI, ALI
10779233	Not Issued	30	02/13/2004	Method and system for LIPf7 origination detection and LIPf8 suppression in a port bypass controller	GHIASI, ALI
10779234	Not Issued	30	02/13/2004	Method and system for seamless dual switching in a port bypass controller	GHIASI, ALI
11268246	Not Issued	30	11/07/2005	Method and system for optimum channel equalization from a SerDes to an optical module	GHIASI, ALI
11491628	Not Issued	25	07/24/2006	Method and system for speed negotiation for twisted pair links using intelligent E-FIFO in fibre channel systems	GHIASI, ALI
11491629	Not Issued	20	07/24/2006	Method and system for speed negotiation for twisted pair links in fibre channel systems	GHIASI, ALI
60397599	Not Issued	159	07/22/2002	High-speed serial bit stream interface and circuitry for supporting such interface	GHIASI, ALI
60398741	Not Issued	159	07/26/2002	High-speed serial bit stream interface and circuitry for supporting such interface	GHIASI, ALI
60401708	Not Issued	159	08/06/2002	Loop back testing structure for high-speed serial bit stream TX and RX chip set	GHIASI, ALI
60401732	Not Issued	159	08/06/2002	High-speed serial bit stream multiplexing and demultiplexing integrated circuits	GHIASI, ALI
60401733	Not Issued	159	08/06/2002	Natural data ordering of a multiplexed high speed bit stream	GHIASI, ALI
60401735	Not Issued	159	08/06/2002	Source centered clock supporting quad 10 GBPS serial interface	GHIASI, ALI
60529143	Not Issued	159	12/12/2003	Method and system for robust elastic FIFO in a port bypass controller	GHIASI, ALI
60529145	Not Issued	159	12/12/2003	Method and system for onboard BER estimation in a port bypass controller	GHIASI, ALI
60529200	Not Issued	159	12/12/2003	Method and system for seamless double switching in a port bypass controller	GHIASI, ALI
60529421	Not Issued	159	12/12/2003	Method and system for LIPF7 origination detection and LIPF8 suppression in a port bypass controller	GHIASI, ALI
60741789	Not Issued	20	12/02/2005	Method and system for speed negotiation for twisted pair links in fibre channel systems	GHIASI, ALI
60742196	Not Issued	20	12/02/2005	Method and system for use of intelligent E-FIFO for speed matching over twisted pair links in fibre channel systems	GHIASI, ALI

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="GHIASI"/>	<input type="text" value="ALI"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)